RoHS Compliant

mSATA Flash Drive

mSATA mini H1 Product Specifications

July 21, 2015

Version 1.3

Apacer
Access the best

Features:

- **Compliance with SATA Revision 3.1**
  - SATA 6.0 Gbps interface
  - Backward compatible with SATA 1.5/3.0 Gbps interfaces
  - ATA command set

- **Capacities**
  - 1, 2, 4, 8, 16, 32 GB

- **Performance***
  - Interface burst read/write: 600 MB/sec
  - Sustained read: up to 65 MB/sec
  - Sustained write: up to 50 MB/sec

- **Flash Management**
  - Built-in hardware ECC
  - Wear leveling
  - Bad block management
  - S.M.A.R.T.
  - Power Failure Management
  - TRIM

- **NAND Flash Type:** SLC

- **Endurance**
  - 1GB:78 TBW
  - 2GB:154 TBW
  - 4GB:310 TBW
  - 8GB: 620 TBW
  - 16GB: 1,231 TBW
  - 32GB: 2,868 TBW

- **Temperature ranges**
  - Operating:
    - Standard: 0°C to 70°C
    - Extended: -40°C to +85°C
  - Storage: -40°C to +100°C

- **Supply voltage**
  - 3.3 V ± 5%

- **Power consumption (typical)***
  - Active mode: 330 mA
  - Idle mode: 100 mA

- **Form factor**
  - mSATA mini, JEDEC MO-300B
  - Dimensions (29.85 x 26.80 x 1.00**, unit: mm)

- **Connector**
  - 52-pin mSATA signal connector

- **RoHS compliant**

- **Thermal sensor (optional)**

- **Device Sleep (optional)**

*Varies from capacities. The values addressed here are typical and may vary depending on host system settings.
**Only the height of the PCB

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1. Product Description

1.1 Introduction

Apacer mSATA mini H1 delivers all the advantages of flash disk technology with the Serial ATA 6.0 Gbps interface and is fully compliant with the standard mSATA mini form factor, known as JEDEC MO-300B standard. The module is designed to operate at optimal efficiency and reliability while maintained in super compact form factor. Meanwhile, the power consumption of the mSATA mini module remains at minimal level.

1.2 Capacity Specification

Table 1-1 Capacity specification

Capacity	Total Bytes	Cylinders	Heads	Sectors	Max LBA
1 GB	1,011,032,064	15,525	16	63	1,974,672
2 GB	2,011,226,112	15,525	16	63	3,928,176
4 GB	4,011,614,208	15,525	16	63	7,835,184
8 GB	8,012,390,400	15,525	16	63	15,649,200
16 GB	16,013,942,784	16,383	16	63	31,277,232
32 GB	32,017,047,552	16,383	16	63	62,533,296

*Display of total bytes varies from file systems.
**Cylinders, heads or sectors are not applicable for these capacities. Only LBA addressing applies.
LBA count addressed in the table above indicates total user storage capacity and will remain the same throughout the lifespan of the device. However, the total usable capacity of the SSD is most likely to be less than the total physical capacity because a small portion of the capacity is reserved for device maintenance usages.

1.3 Performance

Performance of mSATA mini H1 is shown in Table 1-2.

Table 1-2 Performance specification

Performance　　　　Capacity	1 GB	2 GB	4 GB	8 GB	16 GB	32 GB
Sustained Read (MB/s)	30	65	65	65	65	65
Sustained Write (MB/s)	20	40	50	50	50	50

Note: Performance varies from flash configurations or host system settings.

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1.4 Pin Assignments

Figure 1-2 Apacer mSATA mini H1 pin assignment

Table 1-3 Pin Assignment Description

Pin #	mSATA Pin	Description
1	NC	No Connect
2	+3.3V	3.3V Source
3	NC	No Connect
4	DGND	Digital GND
5	NC	No Connect
6	NC	No Connect
7	NC	No Connect
8	NC	No Connect
9	DGND	Digital GND
10	NC	No Connect
11	NC	No Connect
12	NC	No Connect
13	NC	No Connect
14	NC	No Connect
15	DGND	Digital GND
16	NC	No Connect
17	NC	No Connect
18	DGND	Digital GND

19	NC	No Connect
20	NC	No Connect
21	SATA GND	SATA Ground Return Pin
22	NC	No Connect
23	+B	Host Receiver Differential Signal Pair
24	+3.3V	3.3V Source
25	-B	Host Receiver Differential Signal Pair
26	SATA GND	SATA Ground Return Pin
27	SATA GND	SATA Ground Return Pin
28	NC	No Connect
29	SATA GND	SATA Ground Return Pin
30	NC	No Connect
31	-A	Host Transmitter Differential Signal Pair
32	NC	No Connect
33	+A	Host Transmitter Differential Signal Pair
34	DGND	Digital GND
35	SATA GND	SATA Ground Return Pin
36	NC	No Connect
37	SATA GND	SATA Ground Return Pin
38	NC	No Connect
39	+3.3V	3.3V Source
40	DGND	Digital GND
41	+3.3V	3.3V Source
42	NC	No Connect
43	NC	No Connect
44	NC	No Connect or Device Sleep*
45	Vendor	Vendor specific
46	NC	No Connect
47	Vendor	Vendor specific
48	NC	No Connect
49	DAS	Device activity signal
50	DGND	Digital GND
51	GND	Default connect to GND
52	+3.3V	3.3V Source

*Device Sleep is optional

2. Software Interface

2.1 Command Set

Table 2-1 summarizes the ATA commands supported by mSATA mini H1.

Table 2-1: Command set

Command	Code	Command	Code
NOP	00h	Idle	97h
Data Set Management	06h	Check Power Mode	98h
Recalibrate	10F-1Fh	Sleep	99h
Read Sectors	20h	SMART	B0h
Read Sectors without Retry	21h	Device Configuration	B1h
Read Sectors EXT	24h	Read Multiple	C4h
Read DMA EXT	25h	Write Multiple	C5h
Read Native Max Address EXT	27h	Set Multiple Mode	C6h
Read Multiple EXT	29h	Read DMA	C8h
Read Log EXT	2Fh	Read DMA without Retry	C9h
Write Sectors	30h	Write DMA	CAh
Write Sectors Without Retry	31h	Write DMA without Retry	CBh
Write Sectors EXT	34h	Write Multiple FUA EXT	CEh
Write DMA EXT	35h	Standby Immediate	E0h
Set Native Max Address EXT	37h	Idle Immediate	E1h
CFA Write Sectors without erase	38h	Standby	E2h
Write Multiple EXT	39h	Idle	E3h
Write DMA FUA EXT	3Dh	Read Buffer	E4h
Write Long EXT	3Fh	Check Power Mode	E5h
Read Verify Sectors	40h	Sleep	E6h
Read Verify Sectors without Retry	41h	Flush Cache	E7h
Read Verify Sectors EXT	42h	Write Buffer	E8h
Write Uncorrectable EXT	45h	Flush Cache EXT	EAh
Read FPDMA Queued	60h	Identify Device	ECh
Write FPDMA Queued	61h	Set Features	EFh
Seek	70h-7Fh	Security Set Password	F1h
Execute Device Diagnostic	90h	Security Unlock	F2h
Initialize Device Parameters	91h	Security Erase Prepare	F3h
Download Microcode	92h	Security Erase Unit	F4h
Download Microcode DMA	93h	Security Freeze Lock	F5h
Standby Immediate	94h	Security Disable Password	F6h
Idle Immediate	95h	Read Native Max Address	F8h
Standby	96h	Set Max Address	F9h

Note. Security command will only be workable when device runs in non-OPAL compliant mode.

2.2 S.M.A.R.T.

SMART, an acronym for Self-Monitoring, Analysis and Reporting Technology, is an open standard that allows a hard disk drive to automatically detect its health and report potential failures. When a failure is recorded by SMART, users can choose to replace the drive to prevent unexpected outage or data loss. Moreover, SMART can inform users of impending failures while there is still time to perform proactive actions, such as copy data to another device.

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3. Flash Management

3.1 Error Correction/Detection

Flash memory cells will deteriorate with use, which might generate random bit errors in the stored data. Thus, this mSATA SSD module applies the BCH ECC Algorithm, which can detect and correct errors occur during Read process, ensure data been read correctly, as well as protect data from corruption. This device can correct up to 72bit/1K data.

3.2 Bad Block Management

Bad blocks are blocks that include one or more invalid bits, and their reliability is not guaranteed. Blocks that are identified and marked as bad by the manufacturer are referred to as "Initial Bad Blocks". Bad blocks that are developed during the lifespan of the flash are named "Later Bad Blocks". Apacer implements an efficient bad block management algorithm to detect the factory-produced bad blocks and manages any bad blocks that appear with use. This practice further prevents data being stored into bad blocks and improves the data reliability.

3.3 Wear Leveling

NAND Flash devices can only undergo a limited number of program/erase cycles, and in most cases, the flash media are not used evenly. If some areas get updated more frequently than others, the lifetime of the device would be reduced significantly. Thus, Wear Leveling technique is applied to extend the lifespan of NAND Flash by evenly distributing write and erase cycles across the media.

Apacer provides advanced Wear Leveling algorithm, which can efficiently spread out the flash usage through the whole flash media area. Moreover, by implementing both dynamic and static Wear Leveling algorithms, the life expectancy of the NAND Flash is greatly improved.

3.4 Power Failure Management

Power Failure Management plays a crucial role when experiencing unstable power supply. Power disruption may occur when users are storing data into the SSD. In this urgent situation, the controller would run multiple flush cycles to store the metadata for later block rebuilding. This urgent operation requires about several milliseconds to get it done. At the next power up, the firmware will perform a status tracking to retrieve the mapping table and resume previously programmed NAND blocks to check if there is any incompleteness of transmission.

3.5 TRIM

TRIM is a feature which helps improve the read/write performance and speed of solid-state drives (SSD). Unlike hard disk drives (HDD), SSDs are not able to overwrite existing data, so the available space gradually becomes smaller with each use. With the TRIM command, the operating system can inform the SSD which blocks of data are no longer in use and can be removed permanently. Thus, the SSD will perform the erase action, which prevents unused data from occupying blocks all the time.

3.6 Secure Erase

Secure Erase is a standard ATA command and will write all "0xFF" to fully wipe all the data on hard drives and SSDs. When this command is issued, the SSD controller will empty its storage blocks and return to its factory default settings.

Note. Secure erase command belongs to ATA security command.

3.7 SATA Power Management

By complying with SATA 6.0 Gb/s specifications, the mSATA SSD supports Device Sleep power state:

- Device Sleep (DevSleep or DEVSLP): PHY powered down; power consumption $\leq$ 5 mW; host assertion time $\leq$ 10 ms; exit timeout from this state $\leq$ 20 ms (unless specified otherwise in SATA Identify Device Log).

Note: Device Sleep mode is optional, depending on product ordering selections.

4. Environment Specifications

4.1 Environmental

Table 4-1 mSATA mini H1 environmental specifications

Environment	Specification
Temperature	0°C to 70°C (Operating) / -40°C to +85°C (Extended)
	-40℃ to +100℃ (in storage)
Humidity	RH 90% under 40℃
Shock	1500G, 0.5ms
Vibration	20Hz~80Hz/1.52mm (frequency/displacement) 80Hz~2000Hz/20G (frequency/displacement) X, Y, Z axis/60mins each
Drop	80cm free fall, 6 face of each
Bending	≧50N, hold 1min/5times
Torque	0.5N-m or 5deg, hold 5min/5times
ESD	Pass

Note: Shock and Vibration specifications are subject to change without notice.

4.2 Mean Time Between Failures (MTBF)

MTBF, an acronym for Mean Time Between Failures, is a measure of a device's reliability. Its value represents the average time between a repair and the next failure. The measure is typically in units of hours. The higher the MTBF value, the higher the reliability of the device. The predicted result of this mSATA device is higher than 2,000,000 hours.

4.3 Certification and Compliance

mSATA mini H1 complies with the following standards:

- SATA Revision 3.1
- RoHS
- ATA/ATAPI
- FCC
- CE
- BSMI
- MIL-STD-810G

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4.4 Endurance

The endurance of a storage device is predicted by TeraBytes Written based on several factors related to usage, such as the amount of data written into the drive, block management conditions, and daily workload for the drive. Thus, key factors, such as Write Amplifications and the number of P/E cycles, can influence the lifespan of the drive.

Capacity	TeraBytes Written
1 GB	78
2 GB	154
4 GB	310
8 GB	620
16 GB	1,231
32 GB	2,868

Notes:
- The measurement assumes the data written to the SSD for test is under a typical and constant rate.
- The measurement follows the standard metric: 1 TB (Terabyte) = 1000 GB.

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5. Electrical Characteristics

5.1 Operating Voltage

Table 5-1 lists the supply voltage for mSATA mini H1.

Table 5-1 mSATA mini H1 operating voltage

Parameter	Conditions
Supply voltage	3.3V ±5%

5.2 Power Consumption

Table 5-2 lists the power consumption for mSATA mini H1.

Table 5-2 Power consumption (typical)

Capacity / Mode	1 GB	2 GB	4 GB	8 GB	16 GB	32 GB
Active (mA)	190	230	250	310	280	330
Idle (mA)	90	90	90	90	90	100

Note: Power consumptions may vary depending on host system settings.

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6. Mechanical Specifications

6.1 Dimensions

Unit: mm
Tolerance: ± 0.2

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Detail B
Scale: 2:1

Detail A
Scale: 2.5:1

Detail C
Scale: 2:1
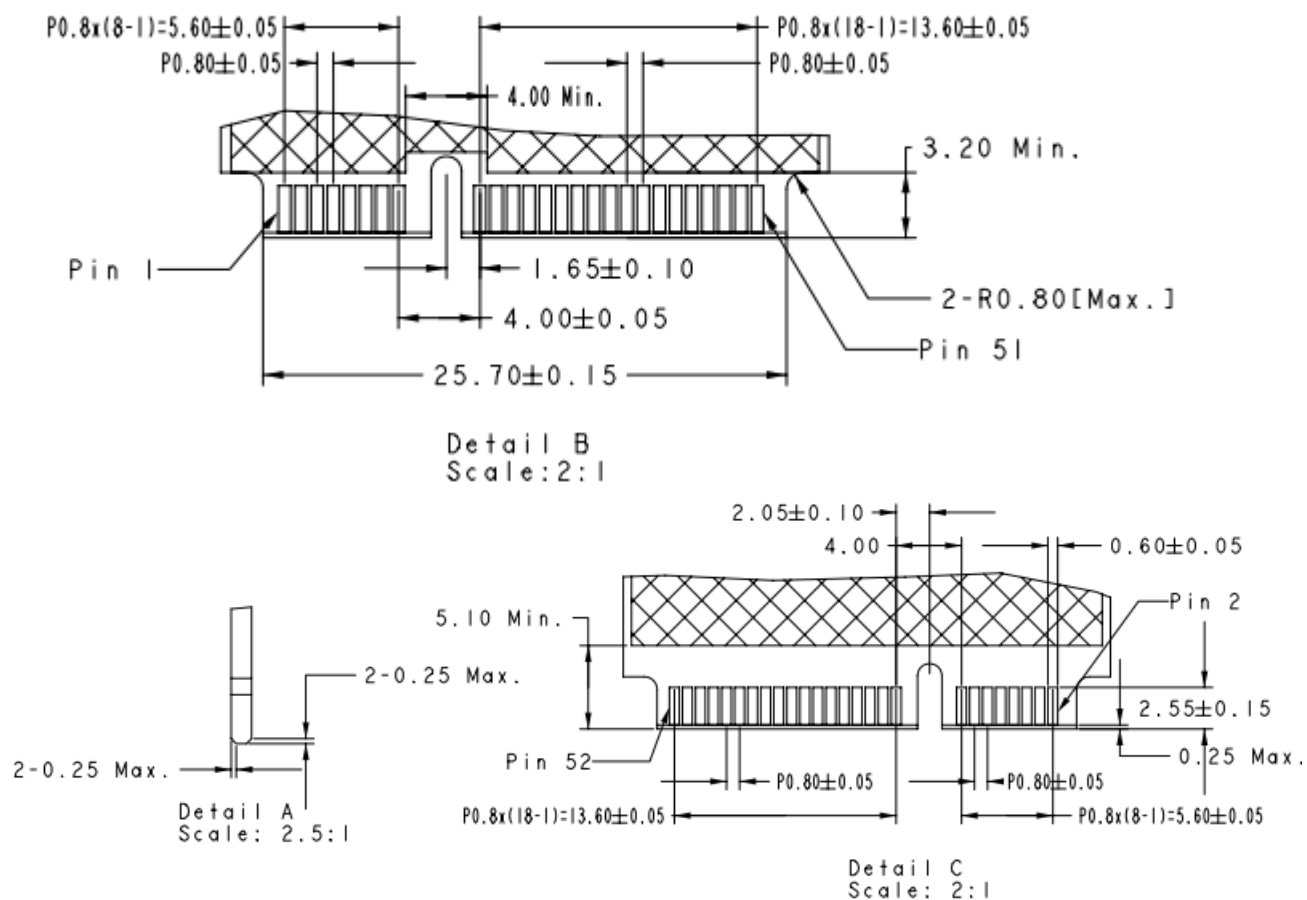
Notes :
1. ▨ = Max Component Height
2. ▨ = No Component
3. ▨ = No Component / Signal Vias / Signal Copper / Printing
4. General Tolerance ±0.1
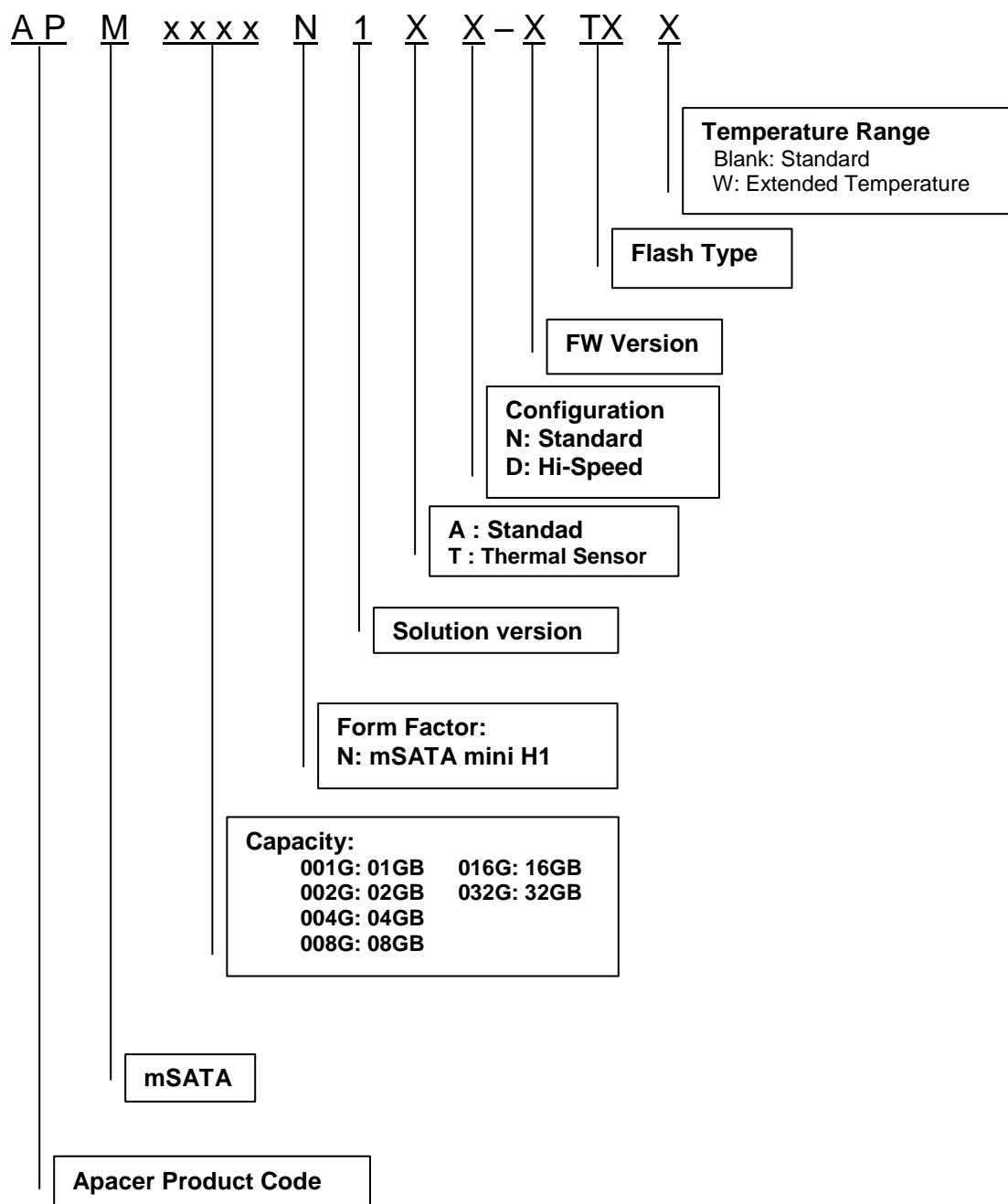
Unit: mm
Tolerance: ± 0.2

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7. Product Ordering Information

7.1 Product Code Designations

A P M x x x x N 1 X X – X TX X

Temperature Range
Blank: Standard
W: Extended Temperature

Flash Type

FW Version

Configuration
N: Standard
D: Hi-Speed

A : Standad
T : Thermal Sensor

Solution version

Form Factor:
N: mSATA mini H1

Capacity:
001G: 01GB	016G: 16GB
002G: 02GB	032G: 32GB
004G: 04GB	
008G: 08GB	

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7.2 Valid Combinations

w/o thermal sensor
7.2.1 w/o DEVSLP

Capacity	Standard	Extended Temperature
1GB	APM001GN1AN-DT	APM001GN1AN-DTW
2GB	APM002GN1AN-DT	APM002GN1AN-DTW
4GB	APM004GN1AN-DT	APM004GN1AN-DTW
8GB	APM008GN1AN-DT	APM008GN1AN-DTW
16GB	APM016GN1AN-DT	APM016GN1AN-DTW
32GB	APM032GN1AN-DT	APM032GN1AN-DTW

7.2.2 w/ DEVSLP

Capacity	Standard	Extended Temperature
1GB	APM001GN1AN-ET	APM001GN1AN-ETW
2GB	APM002GN1AN-ET	APM002GN1AN-ETW
4GB	APM004GN1AN-ET	APM004GN1AN-ETW
8GB	APM008GN1AN-ET	APM008GN1AN-ETW
16GB	APM016GN1AN-ET	APM016GN1AN-ETW
32GB	APM032GN1AN-ET	APM032GN1AN-ETW

w/ thermal sensor
7.2.1 w/o DEVSLP

Capacity	Standard	Extended Temperature
1GB	APM001GN1TN-DT	APM001GN1TN-DTW
2GB	APM002GN1TN-DT	APM002GN1TN-DTW
4GB	APM004GN1TN-DT	APM004GN1TN-DTW
8GB	APM008GN1TN-DT	APM008GN1TN-DTW
16GB	APM016GN1TN-DT	APM016GN1TN-DTW
32GB	APM032GN1TN-DT	APM032GN1TN-DTW

7.2.2 w/ DEVSLP

Capacity	Standard	Extended Temperature
1GB	APM001GN1TN-ET	APM001GN1TN-ETW
2GB	APM002GN1TN-ET	APM002GN1TN-ETW
4GB	APM004GN1TN-ET	APM004GN1TN-ETW
8GB	APM008GN1TN-ET	APM008GN1TN-ETW
16GB	APM016GN1TN-ET	APM016GN1TN-ETW
32GB	APM032GN1TN-ET	APM032GN1TN-ETW

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Revision History

Revision	Description	Date
1.0	First release	05/08/2015
1.1	Removed 64GB capacity support	06/03/2015
1.2	Revised product ordering information	06/16/2015
1.3	Removed 64GB capacity from Product Ordering Information	07/21/2015

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