RoHS Compliant

SATA-Disk Module 4

SDM4-M 7P/90D Middle Profile Specifications

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Version 1.3

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Features:

- **Standard Serial ATA Revision 2.6**
  - Compliance with SATA Revision 2.6 specifications
  - SATA 3.0 Gbps
  - ATA-compatible command set

- **Capacities**
  - 4, 8, 16, 32, 64 GB

- **Performance***
  - Burst read/write: 300 MB/sec
  - Sustained read: up to 85 MB/sec
  - Sustained write: up to 43 MB/sec

- **Intelligent endurance design**
  - Built-in hardware ECC, enabling up to 16/24 bit correction per 1K bytes
  - Static/dynamic wear-leveling
  - Flash bad-block management
  - S.M.A.R.T.
  - Power Failure Management
  - ATA Secure Erase
  - TRIM

- **NAND Flash Type:** SLC

- **Temperature ranges**
  - Operation:
    Standard: 0℃ to 70℃ (32 ~ 158℉)
    Extended: -40℃ to 85℃(-40° ~ 185℉)**
  - Storage: -40℃ to 100℃ (-40° ~ 212℉)

- **Supply voltage**
  - 5.0 V ± 5%

- **Power consumption (typical)***
  - Active mode: 225 mA@5.0 V
  - Idle mode: 83 mA@5.0 V

- **Connector type**
  - 7-pin signal connector
  - Power segment in two types: power cable and power cable-less types

- **Form factor**
  - Middle profile SATA Disk Module
  - Dimension: 58.0 x 24.0 x1.0***, unit: mm

- **Shock & Vibration****
  - Shock: 1500 G
  - Vibration: 15 G

- **MTBF: >**1,000,000 hours

- **Write Protect (optional)**
  - Enabled by onboard hardware switch

- **RoHS compliant**

*Varies from capacities. The values for performances and power consumptions presented are typical and may vary depending on flash configurations or platform settings. The term idle refers to the standby state of the device.
**Only available in 8, 16 and 32GB models.
***Implies to only the thickness of the module, without including the height/thickness of the connector head.
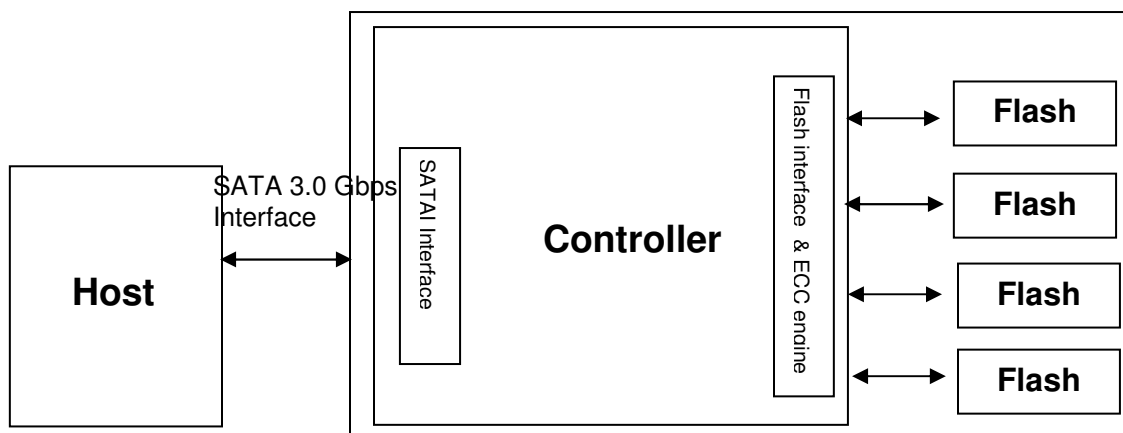****Non-operating

Rev. 1.3

Table of Contents

Rev. 1.3

1. General Description

Apacer's SATA-Disk Module 4 MLC version (SDM4-M) is a high-performance, embedded SSD DOM (Disk on Module) designed to replace the conventional SATA storage devices. SDM4-M can be fitted into a standard Serial ATA connectors commonly found in desktops, IT-STB, industrial PC and thin client systems. In addition, SDM4-M provides an innovative way for design-in clients with compact host computing systems.

Apacer's SDM4-M is built in with an efficient microcontroller that drives the potential of NAND flash and delivers optimal performance. With its compact form factor and SATA 3.0 Gbps interface, this product is well suited for embedded flash storage applications offering customizable and expanded functionalities as well as more cost-effective designs, better performance and increased reliability.

2. Functional Block

SDM4-M includes the SATA and Flash Management controllers, and flash media, as well as the SATA standard interface. Figure 2-1 shows the functional block diagram.

Figure 2-1: Functional block diagram

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3. Pin Assignments

Power Pin 1 (Vcc)
for the Power
Cable-less Type

Signal Pin 1

Power Pin 1 (Vcc)
for the Power
Cable Type

Ground

Ø5.00

Ø3.20
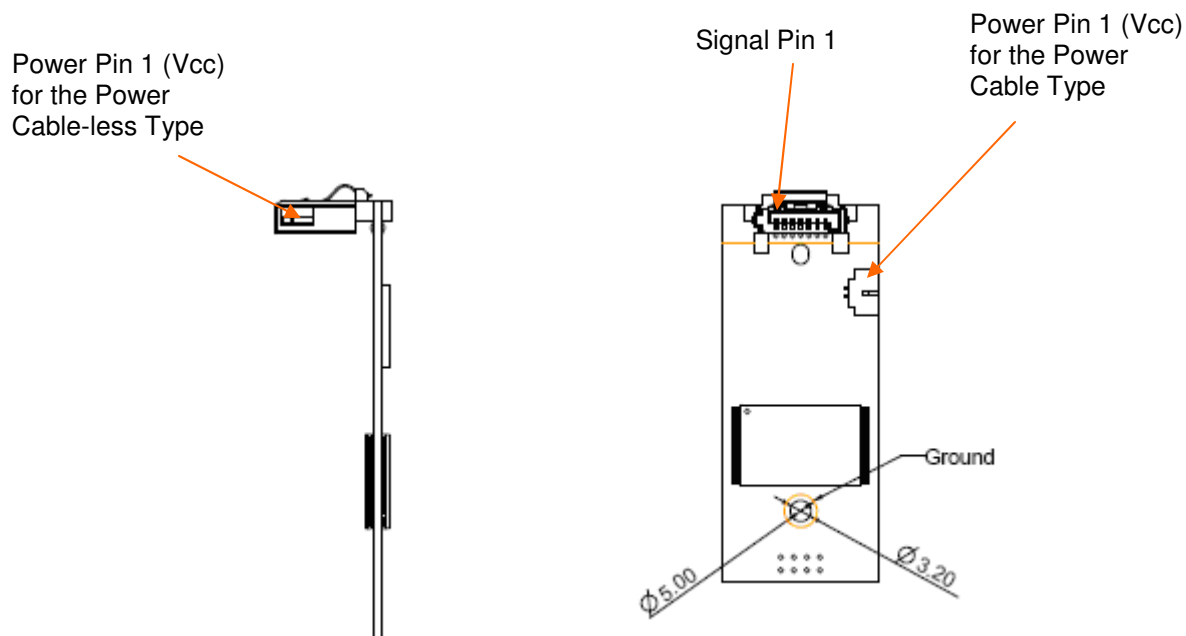
Table 3-1: Signal segment

Name	Type	Description
Pin 1	GND	
Pin 2	RxP	+ Differential Receive Signal
Pin 3	RxN	- Differential Receive Signal
Pin 4	GND	
Pin 5	TxN	- Differential Transmit Signal
Pin 6	TxP	+ Differential Transmit Signal
Pin 7	GND	

Table 3-2: Power segment

Name	Description
Pin 1	VCC (5V)
Pin 2	GND
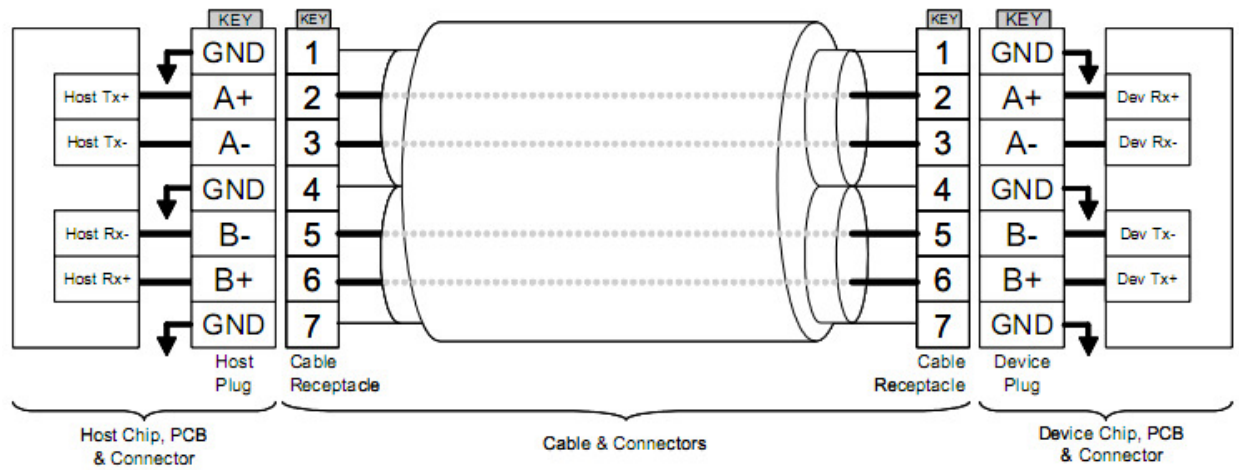
Figure 3-1: SATA Cable/Connector Connection Diagram

The connector on the left represents the Host with TX/RX differential pairs connected to a cable. The connector on the right shows the Device with TX/RX differential pairs also connected to the cable. Notice also the ground path connecting the shielding of the cable to the Cable Receptacle.

Rev. 1.3

4. Product Specification

4.1 Capacity

Capacity specification of SDM4-M product family is available as shown in Table 4-1. It lists the specific capacity and the default numbers of heads, sectors and cylinders for each product line.

Table 4-1: Capacity specifications

Capacity	Total bytes	Cylinders	Heads	Sectors	Max LBA
4 GB	4,011,614,208	7,773	16	63	7,835,184
8 GB	8,012,390,400	15,525	16	63	15,649,200
16 GB	16,013,942,784	16,383	16	63	31,277,232
32 GB	32,017,047,552	16,383	16	63	62,533,296
64 GB	64,023,257,088	16,383	16	63	125,045,424

Notes:
Display of total bytes varies from file systems, which means not all of the bytes can be used for storage.
Cylinders, heads or sectors are not applicable for these capacities. Only LBA addressing applies.
LBA count addressed in the table above indicates total user storage capacity and will remain the same throughout the lifespan of the device. However, the total usable capacity of the SSD is most likely to be less than the total physical capacity because a small portion of the capacity is reserved for device maintenance usages.

4.2 Performance

Performances of SDM4-M are listed below in table 4-2.

Table 4-2: Performance (Typical)

Capacity / Performance	4 GB	8 GB	16 GB	32 GB	64 GB
Sustained read (MB/s)	65	65	70	80	80
Sustained write (MB/s)	12	12	17	43	43

Note: Results may differ from various flash configurations or host system settings.

Rev. 1.3

4.3 Environmental Specifications

Environmental specification of SDM4-M product family follows military standards, indicated in Table 4-3.

Table 4-3 SDM4-M environmental specifications

Item	Specification
Operating temperature	0~70(℃)
Extended temperature	-40 ~ +85(℃)
Storage temperature	-40~100(℃)
Vibration (non-operating)	10~2000(Hz), 15(G), sine wave, X, Y, Z axis
Vibration (operating)	20~2000(Hz), 7.69 (Grms), random wave, X, Y, Z axis
Shock (non-operating)	1500(G) half-sine wave, $\pm X, \pm Y, \pm Z$ axis
Shock (operating)	50(G), half sine wave, $\pm X, \pm Y, \pm Z$ axis

Note: extended operating temperature specifications are only available for 8, 16, 32GB capacities.

4.4 Mean Time Between Failures (MTBF)

Mean Time Between Failures (MTBF) is predicted based on reliability data for the individual components in SDM4-M. The prediction result for SDM4-M is more than 2,000,000 hours.

Notes about the MTBF:

The MTBF is predicated and calculated based on "Telcordia Technologies Special Report, SR-332, Issue 2" method.

4.5 Certification and Compliance

SDM4-M complies with the following standards:

- CE
- FCC
- RoHS
- MIL-STD-810

Rev. 1.3

5. Flash Management

5.1 Error Correction/Detection

SDM4-M implements a hardware ECC scheme, based on the BCH algorithm. It can detect and correct up to 16 bits or 24 bits error in 1K bytes.

5.2 Flash Block Management

Current production technology is unable to guarantee total reliability of NAND flash memory array. When a flash memory device leaves factory, it comes with a minimal number of initial bad blocks during production or out-of-factory as there is no currently known technology that produce flash chips free of bad blocks. In addition, bad blocks may develop during program/erase cycles. When host performs program/erase command on a block, bad block may appear in Status Register. Since bad blocks are inevitable, the solution is to keep them in control. Apacer flash devices are programmed with ECC, block mapping technique and S.M.A.R.T to reduce invalidity or error. Once bad blocks are detected, data in those blocks will be transferred to free blocks and error will be corrected by designated algorithms.

5.3 Wear Leveling

Flash memory devices differ from Hard Disk Drives (HDDs) in terms of how blocks are utilized. For HDDs, when a change is made to stored data, like erase or update, the controller mechanism on HDDs will perform overwrites on blocks. Unlike HDDs, flash blocks cannot be overwritten and each P/E cycle wears down the lifespan of blocks gradually. Repeatedly program/erase cycles performed on the same memory cells will eventually cause some blocks to age faster than others. This would bring flash storages to their end of service term sooner. Wear leveling is an important mechanism that level out the wearing of blocks so that the wearing-down of blocks can be almost evenly distributed. This will increase the lifespan of SSDs. Commonly used wear leveling types are Static and Dynamic.

5.4 Power Failure Management

Power Failure Management plays a crucial role when experiencing unstable power supply. Power disruption may occur when users are storing data into the SSD. In this urgent situation, the controller would run multiple write-to-flash cycles to store the metadata for later block rebuilding. This urgent operation requires about several milliseconds to get it done. At the next power up, the firmware will perform a status tracking to retrieve the mapping table and resume previously programmed NAND blocks to check if there is any incompleteness of transmission.

5.5 ATA Secure Erase

AATA Secure Erase is an ATA disk purging command currently embedded in most of the storage drives. Defined in ATA specifications, (ATA) Secure Erase is part of Security Feature Set that allows storage drives to erase all user data areas. The erase process usually runs on the firmware level as most of the ATA-based storage media currently in the market are built-in with this command. ATA Secure Erase can securely wipe out the user data in the drive and protects it from malicious attack.

Rev. 1.3

5.6 S.M.A.R.T.

S.M.A.R.T. is an abbreviation for Self-Monitoring, Analysis and Reporting Technology, a self-monitoring system that provides indicators of drive health as well as potential disk problems. It serves as a warning for users from unscheduled downtime by monitoring and displaying critical drive information. Ideally, this should allow taking proactive actions to prevent drive failure and make use of S.M.A.R.T. information for future product development reference.

Apacer devices use the standard SMART command B0h to read data out from the drive to activate our S.M.A.R.T. feature that complies with the ATA/ATAPI specifications. S.M.A.R.T. Attribute IDs shall include initial bad block count, total later bad block count, maximum erase count, average erase count, power on hours and power cycle. When the S.M.A.R.T. Utility running on the host, it analyzes and reports the disk status to the host before the device reaches in critical condition.

Note: attribute IDs may vary from product models due to various solution design and supporting capabilities.

5.7 TRIM

TRIM, though in capital letters usually, is a memory computation command rather than an abbreviation. It is mainly a SATA command that enables the operating system to inform the SSD (Solid State Drive) which blocks of previously stored data are no longer valid, due to erases by the host or operating system, such as file deletions or disk formatting. Once notified, SSD will begin the discard of the invalid LBAs and retain more space for itself, in fact, the discarded is no longer recoverable.

When an LBA is replaced by the operating system, as with overwrite of a file, the SSD is informed that the originally occupied LBA is determined as no longer in use or invalid. The SSD will not save those blocks in garbage collected sectors. Noticeably, a file deletion command by host or operating system never actually erases the actual content, rather, just the file is marked as deleted. This issue is even specifically noticeable for flash based memory devices, such as SSDs. In fact, an SSD will keep garbage collecting the invalid, previously occupied LBAs, if it is not informed that these LBAs can be erased. Thus, the SSD would experience a significant performance downfall.

Rev. 1.3

6. Software Interface

6.1 Command Set

This section defines the software requirements and the format of the commands the host sends to SDM4. Commands are issued to SDM4 by loading the required registers in the command block with the supplied parameters, and then writing the command code to the Command register.

Table 6-1: Command set

Code	Command	Code	Command
E5h	Check Power Mode	F3h	Security Erase Prepare
06h	Data Set Management	F4h	Security Erase Unit
90h	Execute Device Diagnostic	F5h	Security Freeze Lock
E7h	Flush Cache	F1h	Security Set Password
EAh	Flush Cache EXT	F2h	Security Unlock
Ech	Identify Device	70h	Seek
E3h	Idle	Efh	Set Features
E1h	Idle Immediate	C6h	Set Multiple Mode
91h	Initialize Device Parameters	E6h	Sleep
C8h	Read DMA	B0h	SMART
25h	Read DMA EXT	E2h	Standby
C4h	Read Multiple	E0h	Standby Immediate
29h	Read Multiple EXT	Cah	Write DMA
20h	Read Sector	35h	Write DMA EXT
24h	Read Sector EXT	C5h	Write Multiple
40h	Read Verify Sectors	39h	Write Multiple EXT
42h	Read Verify Sectors EXT	30h	Write Sector
10h	Recalibrate	34h	Write Sector EXT
F6h	Security Disable Password		

7. Electrical Specification

Table 7-1: Operating range

Item	Description
Required voltage	5V±5% (4.75-5.25V)
Operating temperature	0℃ to +70℃ (standard) -40℃ to 85℃ (extended)

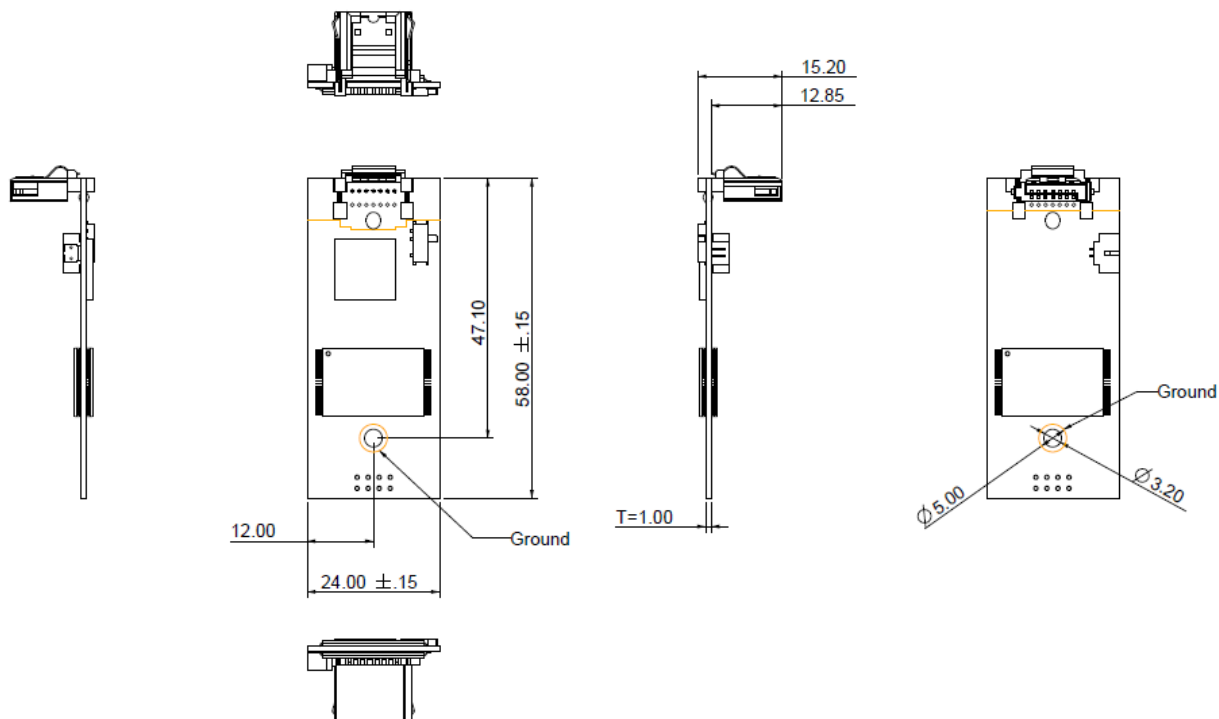Note: extended operating temperature specifications are only available for 8, 16, 32GB capacities.

Table 7-2: Typical power consumption

Capacity Modes	4 GB	8 GB	16 GB	32 GB	64 GB
Active (mA)	178	178	178	210	225
Idle (mA)	83	88	88	88	88

Note: Results may differ from various flash configurations or host system settings.

Rev. 1.3

8. Physical Characteristics

8.1 Power Cable Type

15.20
12.85

47.10

58.00 ±.15

12.00

24.00 ±.15

Ground

T=1.00

Ground

Ø5.00

Ø3.20

Unit: ㎜
Tolerance: ± 0.25

Rev. 1.3

8.2 Power Cable-less Type

15.20
12.85

47.10

58.00 ±.15

12.00
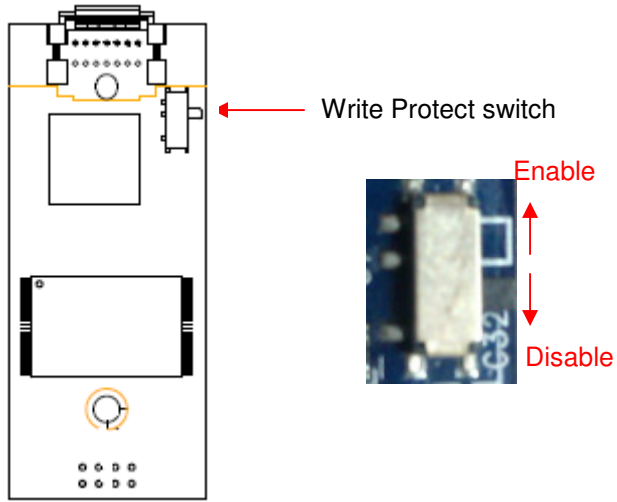
24.00 ±.15

Ground

T=1.00

Ground

Ø5.00

Ø3.20

Unit: ㎜
Tolerance: ± 0.25

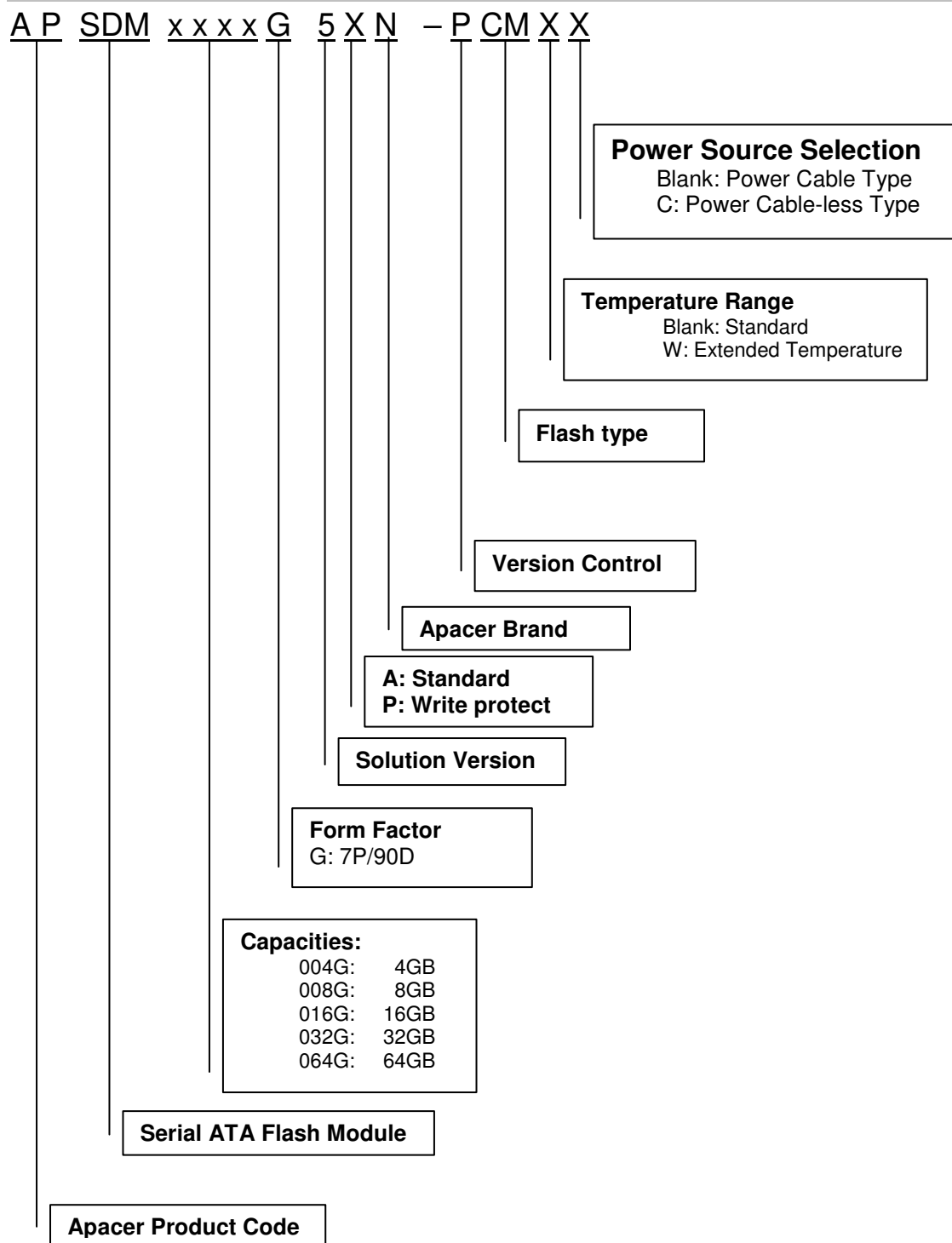Rev. 1.3
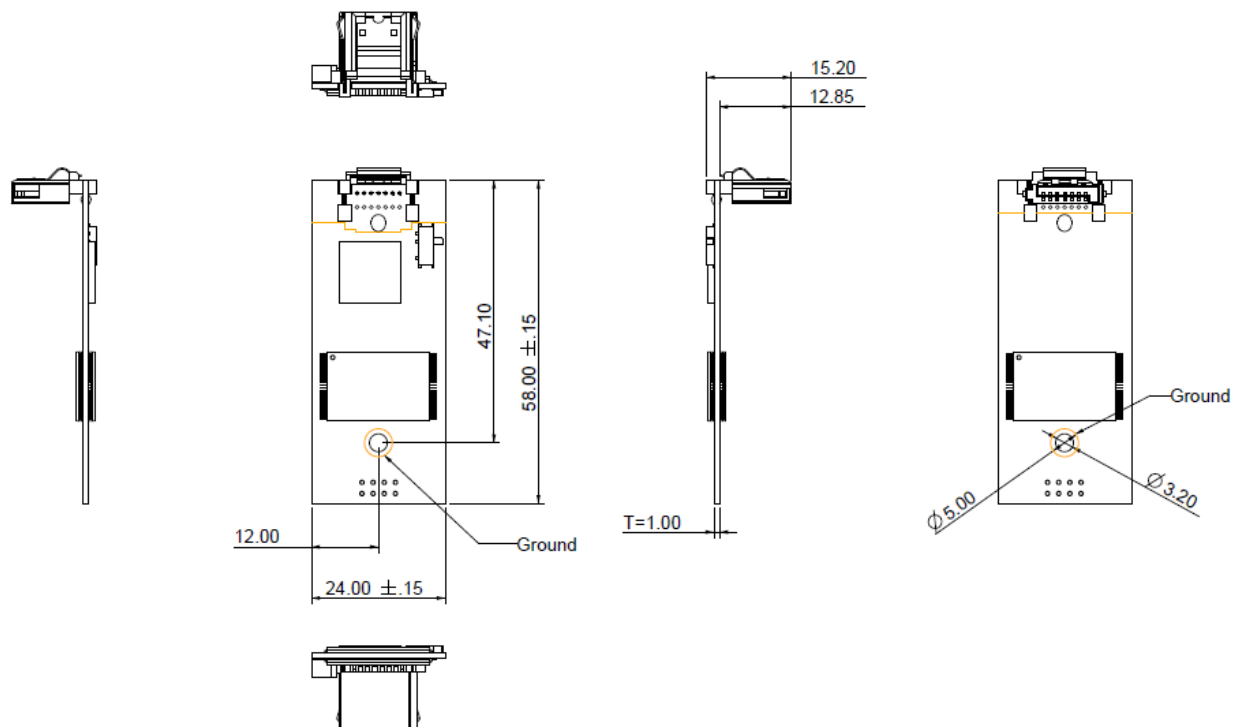
8.3 Write Protect Switch (optional)

Description of Apacer Write Protect:

Apacer implements the Virtual Write scheme that allows write commands to go through the flash controller and data is temporarily stored, but no data has been actually written into the flash. Once the host resets or restarts the system, all the temporarily stored data will disappear and nowhere to be found in the drive. Since the Virtual Write scheme runs at device level, it requires no software or driver installation and is independent from the host OS.

Rev. 1.3

9. Product Ordering Information

9.1 Product Code Designations

A P SDM x x x x G 5 X N – P CM X X

Power Source Selection
Blank: Power Cable Type
C: Power Cable-less Type

Temperature Range
Blank: Standard
W: Extended Temperature

Flash type

Version Control

Apacer Brand

A: Standard
P: Write protect

Solution Version

Form Factor
G: 7P/90D

Capacities:
004G: 4GB
008G: 8GB
016G: 16GB
032G: 32GB
064G: 64GB

Serial ATA Flash Module

Apacer Product Code

9.2 Valid Combinations

7P/90D MP: Power Cable Type

Capacity	Standard	Extended Temperature
4GB	APSDM004GG5AN-PCM	--
8GB	APSDM008GG5AN-PCM	APSDM008GG5AN-PCMW
16GB	APSDM016GG5AN-PCM	APSDM016GG5AN-PCMW
32GB	APSDM032GG5AN-PCM	APSDM032GG5AN-PCMW
64GB	APSDM064GG5AN-PCM	--

7P/90D MP: Power Cable-less Type

Capacity	Standard	Extended Temperature
4GB	APSDM004GG5AN-PCMC	--
8GB	APSDM008GG5AN-PCMC	APSDM008GG5AN-PCMWC
16GB	APSDM016GG5AN-PCMC	APSDM016GG5AN-PCMWC
32GB	APSDM032GG5AN-PCMC	APSDM032GG5AN-PCMWC
64GB	APSDM064GG5AN-PCMC	--

7P/90D MP: Power Cable Type with write protect

Capacity	Standard	Extended Temperature
4GB	APSDM004GG5PN-PCM	--
8GB	APSDM008GG5PN-PCM	APSDM008GG5PN-PCMW
16GB	APSDM016GG5PN-PCM	APSDM016GG5PN-PCMW
32GB	APSDM032GG5PN-PCM	APSDM032GG5PN-PCMW
64GB	APSDM064GG5PN-PCM	--

7P/90D MP: Power Cable-less Type with write protect

Capacity	Standard	Extended Temperature
4GB	APSDM004GG5PN-PCMC	--
8GB	APSDM008GG5PN-PCMC	APSDM008GG5PN-PCMWC
16GB	APSDM016GG5PN-PCMC	APSDM016GG5PN-PCMWC
32GB	APSDM032GG5PN-PCMC	APSDM032GG5PN-PCMWC
64GB	APSDM064GG5PN-PCMC	--

Note: Please consult with Apacer sales representative for availabilities.

Revision History

Revision	Date	Description	Remark
1.0	12/02/2011	Official release	
1.1	1/17/2012	Revised capacity information Updated Product Ordering Information Added Power cable-less model	
1.2	11/09/2012	Updated Product Ordering Information due to firmware upgrade Added Write Protect option	
1.3	07/05/2013	Updated Product Ordering Information due to firmware upgrade	

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Rev. 1.3